

## CLAIM AMENDMENTS

Please amend the claims by canceling claims 1 and 5-25, amending claims 2 and 3, and adding new claims 26-30, all without prejudice, as indicated on the following listing of all the claims in the present application after this Amendment:

1. (Cancelled)
2. (Currently Amended) A method of writing data into a non-volatile memory system of a type having blocks of memory cells that are simultaneously erasable and which individually store a given number of host units of data, comprising:  
responding to host commands to write units of data having non-sequential logical addresses by writing the data ~~with sequential physical~~ having non-sequential logical addresses into a first designated block with sequential physical addresses, and  
responding to host commands to write units of data having sequential logical addresses equal to or in excess of a given proportion of said given number by writing the data into a second designated block.
3. (Currently Amended) The method of claim 2, wherein writing data to the first designated block includes writing a number of host units of data into the first designated block having sequential logical addresses less than the given proportion of said given number.
4. (Original) The method of claim 2, wherein the non-volatile memory cells are organized into multiple sub-arrays and said blocks of memory cells include memory cells of two or more of the sub-arrays.
- 5 – 25. (Cancelled)
26. (New) The method of claim 2, wherein the given proportion is set within a range of 25-75 percent of said given number.

27. (New) The method of claim 3, wherein the given proportion is set within a range of 25-75 percent of said given number.

28. (New) A method of writing data into a non-volatile memory system of a type having blocks of memory cells that are simultaneously erasable and which individually store a given number of host units of data, comprising:

responding to host commands to write units of data having a number of sequential logical addresses less than a fraction of said given number by writing the data into a first designated block, and

responding to host commands to write units of data having a number of sequential logical addresses equal to or in excess of the fraction of said given number by writing the data into a second designated block.

29. (New) The method of claim 28, wherein the non-volatile memory cells are organized into multiple sub-arrays and said blocks of memory cells include memory cells of two or more of the sub-arrays.

30. (New) The method of claim 28, wherein the fraction is set within a range of 25-75 percent of said given number.